

Securect 7 3 Crack 4l

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C:\WINDOWS\system32\command.com
Microsoft(R) Windows DOS
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C:\DOCUMENT~1\WXPU>"C:\Documents and Settings\wxpu\Desktop\Coreinfo.exe"

Coreinfo v3.2 - Dump information on system CPU and memory topology
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Sysinternals - www.sysinternals.com

Pentium(R) Dual-Core CPU E6300 @ 2.80GHz
x86 Family 6 Model 23 Stepping 10, GenuineIntel
HTT - Hyperthreading enabled
HYPERVISOR - Hypervisor is present
VMX - Supports Intel hardware-assisted virtualization
SVM - Supports AMD hardware-assisted virtualization
EM64T - Supports 64-bit mode

SMX - Supports Intel trusted execution
SKINIT - Supports AMD SKINIT

NX - Supports no-execute page protection
SMEP - Supports Supervisor Mode Execution Prevention
SMAP - Supports Supervisor Mode Access Prevention
PAGE1GB - Supports 1 GB large pages
PAE - Supports > 32-bit physical addresses
PAT - Supports Page Attribute Table
PSE - Supports 4 MB pages
PSE36 - Supports > 32-bit address 4 MB pages
PGE - Supports global bit in page tables
SS - Supports bus snooping for cache operations
UME - Supports Virtual-8086 mode
RDWRFSGBASE - Supports direct GS/FS base access

FPU - Implements 1387 floating point instructions
MMX - Supports MMX instruction set
MMXEXT - Implements AMD MMX extensions
3DNow - Supports 3DNow! instructions
3DNowEXT - Supports 3DNow! extension instructions
SSE - Supports Streaming SIMD Extensions
SSE2 - Supports Streaming SIMD Extensions 2
SSE3 - Supports Streaming SIMD Extensions 3
SSSE3 - Supports Supplemental SIMD Extensions 3
SSE4.1 - Supports Streaming SIMD Extensions 4.1
SSE4.2 - Supports Streaming SIMD Extensions 4.2

AES - Supports AES extensions
AUX - Supports AUX instruction extensions
FMA - Supports FMA extensions using YMM state
MSR - Implements RDMSR/WRMSR instructions
MTRR - Supports Memory Type Range Registers
XSAVE - Supports XSAVE/XRSTOR instructions
OSXSAVE - Supports XSETBU/XGETBU instructions
RDRAND - Supports RDRAND instruction
RDSEED - Supports RDSEED instruction
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